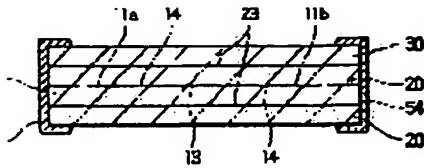


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(57)Abstract:

**PURPOSE:** To prevent reliably the generation of crosstalk between signal lines even if internal conductors, which absorb a high-frequency noise and a surge and are connected to a plurality of the signal lines, are provided in a higher density.

**CONSTITUTION:** The title laminated capacitor array is a laminated material consisting of a dielectric sheet 10 and dielectric sheets 20, which respectively have a capacitive property and varistor characteristics, and the sheet 10 is provided with internal conductors 11a and 11b which are connected to one side of the sheet 10, internal conductors, which are connected to the opposite side to this one side and are insulated from the conductors 11a and 11b, and an isolation conductor 13, which passes between these internal conductors and is connected to another one pair of sides to which the internal conductors are not connected, on the sheet surface. The sheets 20 are respectively provided with a ground conductor 23, which is connected to one pair of sides to correspond to the one pair of sides, to which the conductor 13 is connected, of the sheet 10 and is insulated from another one pair of sides, on the respective sheet surfaces and a capacitance is formed between the internal conductors and the conductor 23 via the sheet 20. Electrodes for signal use, which are connected to the internal conductors, and one pair of electrodes 53 and 54 for grounding use, which are connected to the conductors 13 and 23, are formed on the side surfaces of the laminated material independently of each other.

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(54) LAMINATED CERAMIC CAPACITOR FOR BALANCED LINE